



Fall' 09 ENGN2911X

Reconfigurable Computing: *Accelerate Your Algorithms*

Topics discussed:

- Introduction to programmable logic technology.
- Fundamentals of reconfigurable computing.
- Hardware programming languages (Verilog, SystemC).
- Rapid prototyping with FPGA boards.
- Direct Matlab to hardware synthesis.
- Reconfigurable soft multi-core computing.
- Acceleration of algorithms.
- Hands-on driven assignments and projects.



Learn hands-on skills for real life engineering!

Instructor: Prof. Sherief Reda

Meeting Times: Tu/Th 2:30-3:50 PM.

Location: B & H 194.

First class meeting: Thursday Sept 10th.

Fall'07 webpage:

<http://scale.engin.brown.edu/classes/EN2911XF09>

Email: sherief_reda@brown.edu

Word cloud containing terms: Algorithms, Multi-Core, SystemC, Verilog, FPGA, Prototyping, Accelerate, Matlab, HDL, Programming.