

**EN2911X: Reconfigurable Computing**

**Brown University**

**Division of Engineering**

**EN2911X Reconfigurable Computing**

**Prof. Sherief Reda**

**HW/LAB 02 (25 points). Due Date: Friday, Sept 26<sup>th</sup> 2014.**

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1. [25 points] Follow Tutorial 1 on the class webpage and report (1) the total logic and routing resources utilized by your circuit; (2) a screen capture of your schematic; (3) a screen capture of the RTL circuit view (tools → netlist viewer → RTL viewer); (4) a screen capture of the post-mapped circuit view (tools → netlist viewer → technology map); (5) a screen capture for the floorplan after fitting; (6) functional simulation waveforms; and (7) timing simulation waveforms.