# Accurate Spatial Estimation and Decomposition Techniques for Variability Characterization

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Abstract—In this paper, we show that the impact of process 1 variations on the parametric measurements of semiconductor 2 circuits can be modeled using multivariate statistical techniques. 3 We show that it is possible to devise data transformation 4 methods to model different kinds of measurements such as 5 timing and leakage using multivariate statistical analysis. We use 6 these models to propose new semiconductor spatial estimation and variability decomposition techniques. We demonstrate a 8 new semiconductor spatial estimation technique based on the 9 expectation-maximization algorithm. Our technique can be used 10 to fill in the expected values of measurements at wafer locations 11 12 that have been skipped or missed during parametric testing. Furthermore, we use our proposed spatial estimation method 13 together with nested analysis of variance techniques to arrive to 14 an accurate variability decomposition method. We extensively 15 verify our models and results with timing and leakage vari-16 ability data measurements collected from a large volume of 17 manufactured wafers at 65 nm SOI process. Using this data 18 we explore and quantify the trade-off between the accuracy 19 of estimations and the reductions in the number of required 20 parametric measurements. We demonstrate the superiority of 21 the proposed technique for spatial estimation in comparison 22 to geostatistical Kriging-based estimators and traditional cubic 23 b-spline-based interpolation methods. We also show the impact of 24 wafer sampling techniques on the accuracy of spatial estimation, 25 and we reveal the spatial structure of various variability sources. 26

Index Terms—Analysis, characterization, modeling, variability.

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## I. INTRODUCTION

GGRESSIVE technology scaling has led to large levels of manufacturing process variations due to statistical fluctuations inherent in the manufacturing process. Manifes-31 tations of these variations include gate length variations, line edge roughness, dopant fluctuations, and variations in the dimensions of interconnects [2], [22], [27]. These variations

Manuscript received October 30, 2009; revised April 5, 2010; accepted April 9, 2010. The work of S. Reda was partially supported by the NSF CAREER, under Grant 0952866. An earlier version of this paper appeared in Design, Automation, Test in Europe 2009 [23]. This paper has an expanded coverage and new material, including: (1) a new section for parametric leakage modeling and data transformation; (2) comparisons against geostatistical-based spatial estimation techniques and cubic b-spline interpolation techniques; (3) variability decomposition using nested ANOVA techniques; (4) different scenarios for the applicability of the expectation-maximization algorithm; and (5) new experiments using both timing and leakage variability measurements using a complete set of wafer of measurements.

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Digital Object Identifier 10.1109/TSM.2010.2051752

impact the key electrical parameters of semiconductor devices and interconnects, and correspondingly determine their metrics like performance, power, yield, and reliability [4], [5], [22], [26]. For example, at the time of writing this paper, the price difference between the fastest and slowest versions of a popular 45 nm quad-core processor is more than \$700. Extrapolatary studies predict that the sources and magnitudes of process variabilities will further increase in future technology nodes [3], [8], [14], [20].

Differences in manufacturing outcomes can be attributed to systematic and random variability sources [2]. Systematic sources impact different die or wafers in a deterministic manner, while random variations are unique to each die or wafer or lot. Random variability can be decomposed into different components that reflect the hierarchy implied by the various steps in the semiconductor manufacturing process [9]. This hierarchy includes the following components: within-die variations that arise within the same die: *die-to-die or across* wafer variations that arise among die manufactured on the same wafer; wafer-to-wafer variations that appear in wafers within the same lot; and lot-to-lot variations that arise among different lots. Because different variations are caused by different physical phenomena, identification and estimation of each variation component is critical for determining effective variability reduction techniques [9], [15].

The increased number of variability sources and the corresponding necessary inline characterization test structures lead to increases in the costs and time dedicated to variability characterization during manufacturing [7]. Thus, there is a real incentive for techniques that can reduce the number of needed measurements without compromising the accuracy of variability characterization. Spatial estimation techniques achieve such reduction by substituting large numbers of physical characterization measurements (e.g., full wafer) with a smaller number of measurements and then use this smaller set of measurements to estimate the skipped measurements.

Given the parametric test measurements from the process sensitive test structures, our objectives are: 1) to develop a statistical model that accurately captures variability characterization measurements across the entire process; and 2) to utilize the developed statistical model to devise spatial estimation and variability decomposition techniques that are of benefit to both process engineers and designers. We summarize our contributions as following.

1) We show that multivariate normal (MVN) statistical techniques can be used to model our variability mea-

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surements data across the entire process. In case the
 measurement data does not lend itself to MVN as sumptions, we show that statistical data transformation
 techniques can enable the application of our modeling
 methods. We also describe a procedure to verify the
 accuracy of our multivariate model and to detect any
 outliers in the measurements.

2) We observe that in medium-volume to high-volume 88 semiconductor fabrication there are typically thousands 89 of wafers that are generated using the same process 90 steps. Thus, by exploiting the correlation structure 91 among the measurements at different wafer locations, 92 it is possible to devise a highly accurate framework for 93 spatial estimation. Our proposed approach carries out 94 parametric test measurements at a few sites and then uses 95 the *expectation-maximization algorithm* to estimate the 96 expected values for the measurements at all other sites. 97 The proposed approach reduces drastically the volume 98 of variability characterization measurements needed. aa

3) We use the proposed spatial estimation technique to-100 gether with nested analysis of variance (ANOVA) tech-101 niques to decompose variability into systematic and ran-102 dom variability sources, and we show how the variability 103 sources can be decomposed into: lot-to-lot, wafer-to-104 wafer, die-to-die and within-die components. We also 105 provide analysis techniques to uncover the spatial struc-106 ture in these systematic and random sources. 107

Using thousands of measurements from process sensitive (4)108 test structures, we validate the proposed techniques and 109 demonstrate their applicability and accuracy for timing 110 and leakage variability modeling and estimation. We 111 elucidate: 1) the trade-off between the accuracy of 112 spatial estimation as a function of the number of physical 113 characterization measurements that are available to the 114 estimation algorithm; and 2) the impact of the sampling 115 plan of the measurements on the accuracy of the esti-116 mation results. 117

The organization of this paper is as follows. In Section 118 II, we provide background information on spatial estimation 119 techniques in the literature and nested analysis of variance 120 (nested ANOVA) methods. In Section III, we develop the 121 proposed multivariate statistical framework and describe the 122 makeup of our set of variability measurement data. In Section 123 IV, we describe our spatial estimation techniques which are 124 based on the expectation-maximization algorithm. In Section 125 V, we describe new methods to decompose the observed 126 variations into systematic and random sources and explore the 127 spatial structure of each of the sources. Finally, we summarize 128 the main conclusions of this paper in Section VI. 129

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## II. BACKGROUND

In this section, we discuss two relevant techniques that we use either as bases for some of our analysis techniques or for comparison purposes. In Subsection II-A, we discuss variograms as a technique to model spatial correlations and Kriging estimators as one potential technique for estimating missing data in spatial fields. In Subsection II-B, we discuss



Fig. 1. Variograms of 10 random fields generated for a hypothetical field of 10 mm  $\times$  10 mm and with a  $\lambda = 0.5$ . The *x*-axis of the plot gives the lag (*h*) and the *y*-axis gives the variogram function  $\gamma(h)$ .

nested analysis of variance techniques which can be used to estimate the contributions of various variability sources.

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#### A. Variograms and Kriging-Based Estimation

Geostatistical-based techniques have been recently proposed 140 as a general framework for spatial variability estimation [18]. 141 In this framework the variability measurement data is assumed 142 to form a 2-D spatial Gaussian random field that is stationary, 143 i.e., the mean and variance of a random variable do not 144 depend on the variable's location in the field, and that the 145 covariance  $C(Y(l_i), Y(l_i))$  between two random variables  $Y(l_i)$ 146 and  $Y(l_i)$  at locations  $l_i$  and  $l_j$ , respectively, depends only 147 on the (Euclidean) distance  $h = ||l_i - l_j||$  between the two 148 locations. That is 149

## $C(Y(l_i), Y(l_i)) = \sigma^2 \varrho(h)$

where the parameter  $\sigma > 0$  is a *scale parameter* ( $\sigma^2$  is the variance of the field) and the function  $\rho$  is called the *correlation function*. A simple and natural model that allows for correlation between different locations is the exponential model. For this model, the correlation function decays exponentially as a function of the distance *h*, that is

$$\varrho(h) = e^{-\lambda h} \quad \lambda > 0. \tag{1}$$

Note that as  $\lambda$  increases the correlation decays faster as a function of the distance. In this respect,  $\lambda$  can be interpreted as the strength of correlation. Under this model, the random field has three parameters: the mean level  $\mu$ , the scale parameter  $\sigma$ , and the strength of correlation  $\lambda$ . In many occasions, this spatial correlation is expressed using the *variogram* function, where the variogram  $\gamma(h)$  is defined as

$$2\gamma(h) = E[((Y(l_i) - Y(l_i + h))^2] = 2\sigma^2(1 - \varrho(h)].$$
(2)

For example, Fig. 1 gives examples of variograms of ten synthetic random fields generated for a hypothetical spatial field with dimensions 10 mm  $\times$  10 mm with a  $\lambda$  = 0.5 and 165

 $\sigma^2 = 1$ . We use the procedure described by Hargreaves *et al.* 

<sup>167</sup> [13] for generating the synthetic random fields.

*Kriging* is a linear estimator that estimates the value at a desired unknown location of a random field as a weighted linear combination of the measurements at known locations of the field. That is

$$Y^*(l_u) = \sum_{i=1}^k \alpha_i Y(l_i) \tag{3}$$

where  $Y(l_i)$  are the measurements at the *k* known locations, Y<sup>\*</sup>( $l_u$ ) is the estimate at the unknown location  $l_u$ , and  $\alpha_i$  are the linear combination weights. Kriging estimation finds the optimal weights that minimize the error variance as computed by  $E[(Y^*(l_u) - Y(l_u))^2]$ . It can be shown [24] that the optimal weights satisfy the following set of linear equations

$$\sum_{j=1}^{k} \alpha_i C(Y(l_i), Y(l_j)) = C(Y(l_u), Y(l_i)) \quad \text{for } i = 1, \dots, k.$$
(4)

The covariances in the linear equations described by (4) can be computed directly through the variogram function of (2). Then, these *k* linear equations can be solved with standard linear algebra techniques to find the  $\alpha_i$ 's required for Kriging estimation as described by (3).

While the utilization of Kriging estimators for semiconduc-183 tor spatial estimation has provided encouraging results [18], 184 the technique cannot leverage measurements from various 185 manufactured wafers toward the estimations required for a 186 particular wafer. Kriging estimation is good for geostatistical 187 studies where there is typically only one set of measurements 188 on a given domain; however, in semiconductor fabrication 189 there are typically hundreds and thousands of wafers that are 190 generated roughly using the same process. The covariance 191 structure between the various measurements on the different 192 wafers can be exploited to provide far accurate measurements 193 194 as we will demonstrate in Section IV.

### 195 B. ANOVA Techniques

The hierarchical decomposition of variability in semi-196 conductor fabrication mirrors the division of variability in 197 many other batch manufacturing systems [17], [15]. In these 198 systems, nested ANOVA methods are used to decompose 199 the total observed variance into the components of inter-200 est. Consider the simplest case of batch manufacturing in 201 which sample products are manufactured in batches, where 202 each batch contains a number of samples. We would have 203 a two-factor ANOVA design: Factor A is responsible for 204 batch-by-batch variations, and Factor B is responsible for 205 sample-to-sample variations. In this case, the change of 206 *Factor B* is nested within the change of *Factor A*, which would 207 be typically written as B(A). If  $y_{ii}$  denotes the measurement 208 taken from sample  $j \in \{1, ..., s\}$  of batch  $i \in \{1, ..., b\}$  then 209  $y_{ij}$  can be expressed as  $y_{ij} = \mu + \alpha_i + \beta_{j(i)}$ . The overall variability 210



Fig. 2. Locations of PSROs within the layout of the chip.

as computed by the total sum of squares is equal to

$$SS_T = \sum_{i=1}^{b} \sum_{j=1}^{s} (y_{ij} - \mu)^2.$$
 (5)

The within batch variability expressed as the sum of squares <sup>212</sup> is equal to <sup>213</sup>

$$SS_{B(A)} = \sum_{i=1}^{b} \sum_{j=1}^{s} (y_{ij} - \bar{y}_{i.})^2$$
(6)

where  $\bar{y}_{i.} = \sum_{j=1}^{b} y_{ij}/b$ . The across batch variability expressed 214 as the sum of squares is equal to 215

$$SS_A = s \times \sum_{i=1}^{b} (\bar{y}_{i.} - \mu)^2.$$
 (7)

It can be shown that  $SS_T = SS_A + SS_{B(A)}$  [17], and hence it is 216 possible to decompose the total variability into two factors or 217 components that reflect the hierarchical manufacturing struc-218 ture. In an analogous method, it is possible to generalize the 219 given two-factor nested model to nested models with a larger 220 number of factors. We will use this technique in Section V to 221 decompose the total variability of our measurement data into 222 lot-to-lot, wafer-to-wafer, die-to-die, and within-die variations. 223

## III. PROPOSED VARIABILITY CHARACTERIZATION MODEL 224

Parametric measurements occur throughout the fabrication 225 process to track its quality control [1], [11], [21]. These mea-226 surements probe various process characteristics such as critical 227 dimensions, transistor thresholds, timing and leakage. Our 228 parametric data set comes from a 65 nm SOI semiconductor 229 process, and the obtained measurements include: 1) frequency 230 measurements from process sensitive ring oscillators (PSROs), 231 where the frequencies are normalized by the number of 232 stages in the ring oscillator design; and 2) leakage (IDDQ) 233 measurements. 234

Each instance of our production chip holds 14 PSROs 235 that are spatially organized along a grid that spans the die's 236 area as shown in Fig. 2. The PSROs are used as a quick 237



Fig. 3. Correlation between the measurements of PSRO number 4 and the actual speed measurements of the part of the chip that it resides in.

parametric indicator of the overall process quality. Previous 238 results in the literature demonstrate that the speeds of PSROs 239 are strongly correlated with the speeds of their embedded 240 chips [1], [12]. In Fig. 3, we give the measurements of the 241 fourth PSRO together with the actual timing results of the 242 part of the chip that it resides in. Our measurements indicate a 243 very strong correlation with a coefficient of 0.89. The leakage 244 of each chip is evaluated using one lumped measurement. 245 Our data set consists of measurements from 244 wafers that 246 span 19 wafer lots and where each wafer holds 111 die. 247 In each wafer few parametric measurements are missing. 248 These missing measurements arise from a number of reasons 249 including, for example, errors in test probe landing locations, 250 failure in test probes, and manufacturing defects. The locations 251 of the missing measurements are typically random and differ 252 depending on the wafer. Furthermore, a few wafers have 253 received "special" processing steps that are different from the 254 rest of the wafers. 255

In this section, we describe the proposed multivariate statis-256 tical modeling framework that will be used to model our vari-257 ability measurements data. In Subsection III-A, we describe 258 the main assumptions of our multivariate statistical techniques. 259 To address the problem of variability measurements that might 260 not lend themselves to our statistical assumptions, we propose 261 data transformation techniques in Subsection III-B. In Sub-262 section III-C, we describe techniques to verify the correctness 263 of our statistical assumptions and to detect any outliers in the 264 data set of measurements. 265

## 266 A. Multivariate Normal Modeling

In this paper, we assume that a given data set of 267 variability measurements comes from a MVN distribution 268 with potentially a few outliers. Even if the data set does 269 not lend itself directly to this assumption, we provide in 270 Subsection III-B data transformation techniques that enable 271 us to carry out such assumption safely. In our model, we 272 consider the measurements obtained from a single wafer as 273 an observation that is mathematically represented as a random 274 vector w. Each observation vector w consists of measurements 275

on *p* variables corresponding to the number of parametric test sites on each wafer. The number of variables or sites is equal to  $p = d \times r$ , where *d* is the number of die on a wafer and *r* is the number of measurements per die.<sup>1</sup> In the MVN distribution, the probability density function for a random vector **w** is equal to 280

$$p(\mathbf{w}) = \frac{1}{(2\pi)^{p/2} \sqrt{|\Sigma|}} e^{-\frac{(\mathbf{w}-\mu)'\Sigma^{-1}(\mathbf{w}-\mu)}{2}}$$
(8)

where  $\mu$  is a  $p \times 1$  vector that gives the expected value of the random vector **w**, and  $\Sigma$  is the  $p \times p$  covariance matrix. If  $\mathbf{w_1}, \mathbf{w_2}, \dots, \mathbf{w_n}$  denote the measurements from some *n* wafers, then the joint density function of all observations is the product of the marginal normal densities 286

$$\begin{cases} \text{joint density} \\ \mathbf{w}_{1}, \mathbf{w}_{2}, \dots, \mathbf{w}_{n} \end{cases} = \prod_{j=1}^{n} p(\mathbf{w}_{j}) \\ = \frac{1}{(2\pi)^{\frac{np}{2}} |\Sigma|^{\frac{n}{2}}} e^{-\frac{1}{2} \sum_{j=1}^{n} (\mathbf{w}_{j} - \mu)' \Sigma^{-1} (\mathbf{w}_{j} - \mu).} \end{cases}$$
(9)

Equation (9) considered as a function of  $\mu$  and  $\Sigma$  for the fixed set of observations  $w_1, w_2, \ldots, w_n$  is called the *likelihood* function, and 285

$$\hat{\mu} = \frac{1}{n} \sum_{j=1}^{n} \mathbf{w}_{j} \tag{10}$$

$$\hat{\Sigma} = \frac{1}{n} \sum_{j=1}^{n} (\mathbf{w}_{j} - \hat{\mu}) (\mathbf{w}_{j} - \hat{\mu})'$$
(11)

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are the maximum likelihood estimators (MLE) of  $\mu$  and  $\Sigma$ , 290 respectively [16].

#### B. Data Transformations

In many cases variability characterization data (e.g., leak-293 age) might not appear MVN. In this case, it is necessary to 294 transform the data to make it MVN. Transforming the data is 295 just a re-expression of the data in different units [16]. The 296 appropriate transformation could be based on a theoretical 297 basis given the nature of the variability characterization data; 298 for example, leakage has exponential dependency on gate 299 length which may suggest using lognormal transformations. 300 Another approach for transformation is based on using the 301 data itself. This latter approach works when a physical model 302 is hard to describe accurately. One of the popular methods 303 for data transformation is the Box-Cox method [6]. For 304 a univariate distribution, the Box-Cox transformation of a 305 random variable x is given by 306

$$x^{(t)} = \begin{cases} \frac{x^{t-1}}{t} & t \neq 0\\ \ln x & t = 0. \end{cases}$$
(12)

Given a set of observations  $x_1, \ldots, x_n$ , the appropriate 307 transformation *t* is the one that maximizes the likelihood of the 308

<sup>&</sup>lt;sup>1</sup>If the size of the data set (that is the number of observations) is relatively small compared to  $p = d \times r$ , then the data can be modeled using *r* independent MVN distributions each with dimension *p*.



Fig. 4. Plot of likelihood function as given by (13) as a function of t to find the best transformation for the leakage measurements of the center die across all wafers.

309 following expression:

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$$L(t) = -\frac{n}{2} \ln \left( \frac{1}{n} \sum_{j=1}^{n} (x_j^{(t)} - \overline{x^{(t)}})^2 \right) + (t-1) \sum_{j=1}^{n} \ln x_j \quad (13)$$

where  $\overline{x^{(t)}}$  is the arithmetic average value of transformed obser-310 vations. The maximum value of the likelihood function of (13) 311 occurs when dL/dt = 0, and it can be readily found through 312 numerical techniques. In our particular implementation, we 313 use the Nelder-Mead algorithm which is an unconstrained 314 nonlinear minimization algorithm [19]. For example, Fig. 4 315 gives the likelihood of the leakage measurements of the 316 center die from all wafers as a function of the Box-Cox 317 transformation value t. From the plot, it is apparent that 318 t = -1.5235 maximizes the likelihood function, and thus, 319 it is the best choice to transform the leakage measurements 320 to look normal. For illustration, we plot the delay-leakage 321 measurements (before and after transformation) in Fig. 5. 322

To verify that the Box-Cox transformation provides a 323 better transformation technique than the standard logarithm 324 transformation for leakage power, we compare the normality 325 of the resultant data from the two transformations. We conduct 326 the Kolmogorov-Smirnov test on both sets of the transformed 327 data under the null hypothesis that the data has a normal 328 distribution. The null hypothesis was rejected at the 5% 329 significance level for the logarithm transformed data but was 330 accepted for the Box-Cox transformed data. In accordance 331 with the Kolmogorov-Smirnov test, we measure the maximum 332 distance between the cumulative distribution function (CDF) 333 of the transformed data and the ideal normal distribution as 334 shown in Fig. 6. The Box-Cox transformed report a distance 335 of 1.3 while the logarithm transformed data report a distance 336 of 3.05, which further confirms that the Box-Cox transformed 337 data is closer to normality than the standard logarithm trans-338 formation. 339

As a wafer observation has many die rather than a single one, it is necessary to devise data transformation techniques that are applicable for multivariate observations. In this case, a power transformation must be selected for each of the variables of the multivariate distribution. If  $t_1, t_2, \ldots, t_p$  denote the power transformations of the *p* variables, then a multivariate observation  $\mathbf{w_j} = (x_{j1}, x_{j2}, \ldots, x_{jp})$  is transformed to  $\mathbf{w_j}^{(t)} =$ 



Fig. 5. Application of Box–Cox transformation to transform nonnormal leakage measurements (of the center die across all wafers) to normal data.



Fig. 6. Empirical CDF plot of leakage power after transformation.

 $(x_{j1}^{(t_1)}, x_{j2}^{(t_2)}, \ldots, x_{jp}^{(t_p)})$ . Each  $t_i$  can be computed by using the maximum likelihood approach on each individual variable as given by (13). While applying the transformation on variables individually does not ensure that the joint distribution is normal, in many cases such approach is practically good enough [16].

## C. Verifying the MVN Assumption

It is necessary to verify the MVN assumption on either the original data or the transformed data set. A formal way to assess the joint normality of a data set is based on calculating the squared *Mahalanobis* distances  $d_j^2$  of the observations, where 358

$$d_i^2 = (\mathbf{w_j} - \mu)' \Sigma^{-1} (\mathbf{w_j} - \mu) \quad j = 1, \dots, n.$$
 (14)

Essentially (14) summarizes all the measurements of a wafer by a single number. It can be shown [16] that 360

$$d_j^2 = (\mathbf{w_j} - \mu)' \Sigma^{-1} (\mathbf{w_j} - \mu) = \sum_{i=1}^p z_i^2$$
(15)



Fig. 7. (a) Chi-square plot for the entire data set. (b) Chi-square plot after removing the few outlier wafers. The x-axis of the plots gives the chi-square quantiles, and the y-axis gives the Mahalanobis distance quantiles.

where  $z_1, z_2, ..., z_p$  are independent standard normal variables. Since  $\sum_{i=1}^{p} z_i^2 = \chi_p^2$ , then the *Mahalanobis* distance has a 361 362 chi-square distribution with p-degrees of freedom. We can as-363 sess the multivariate normality of a data set of  $w_1, w_2, \ldots, w_n$ 364 by plotting the *chi-square plot* of the Mahalanobis distances 365 of its wafer observations (either directly or after an applicable 366 data transformation). The *chi-square plot* is a generalization 367 of the Q-Q plot for the case of MVN observations. We use 368 the following standard procedure to construct the chi-square 369 plot. 370

1) Use (14) to compute the squared Mahalanobis distances 371 for each of the *n* wafers. Then, order the computed dis-372 tances from smallest to largest  $d_{(1)}^2 \le d_{(2)}^2 \le \cdots \le d_{(n)}^2$ . 373 2) On a 2-D plane, plot the pairs  $(q_p((j-\frac{1}{2})/n), d_j^2)$ , 374 where  $q_n((j-1/2)/n)$  is the  $100((j-\frac{1}{2})/n)$  quantile 375 of the standard chi-square distribution with p degrees 376 of freedom. 377

If the MVN assumption is indeed true, then the chi-square plot 378 should be a straight line that passes through the origin of the 379 plane with a slope equal to 1. 380

To tune the fabrication process, process engineers occa-381 sionally experiment with some wafers to assess any proposed 382 process changes. The measurements of these wafers, which 383 are included with the rest of the measurements, may constitute 384 unusual observations, or outliers, within the population of ob-385 servations. One method to detect outlier wafers is to examine 386 the calculated Mahalanobis distances using (14) for unusually 387 large numbers. In a chi-square plot, the outliers would be the 388 points farthest from the origin. 389

Fig. 7(a) gives the chi-square plot for our entire data set of 390 PSRO frequency measurements. The x-axis of the plot gives 391 the chi-square quantiles, and the y-axis gives the Mahalanobis 392 distance quantiles. As it is clear from the figure, there are 393 points with extreme values that are apart from the rest of the 394 bulk points. Checking these points against the manufacturing 395 recipes of the different wafers confirmed that the wafers cor-396 responding to these points received special processing steps. 397 After removing these few outlier wafers from our data set, we 398 re-plot the chi-square plot in Fig. 7(b). In contrast to Fig. 7(a), 399 Fig. 7(b) displays quite a linear plot that passes close to the 400 origin with a near unity slope. The linearity of the chi-square 401 plot verifies that our MVN assumption is an accurate way to 402 model the parametric measurements in hand. 403

## **IV. PROPOSED SPATIAL VARIABILITY ESTIMATION** TECHNIOUE

Based on the proposed statistical model, we propose in 406 this section a new method for spatial estimation of semi-407 conductor variability measurements. There are two reasons 408 that motivate spatial estimation of variability measurements. 409 The first reason is that in each wafer, a good number of 410 the parametric measurements are naturally missing because of 411 errors in test probe landing locations, failure in test probes, 412 and manufacturing defects. The locations of the missing 413 measurements are typically random and differ depending on 414 the wafer. The second reason is that process engineers could 415 intentionally skip some sites from measuring due to limitations 416 in the cost and time required for variability measurements. 417 Our method substitutes expensive physical measurements by 418 soft computational methods that are capable of accurately 419 estimating the expected values of the skipped measurements. 420

## A. Proposed Method

To compute the MLE distribution parameters from incom-422 plete data, we propose utilizing the expectation maximization 423 (EM) algorithm [10]. The EM algorithm enables parameter 424 estimation in multivariate statistical models with incomplete 425 data. The algorithm is an iterative procedure for estimating 426 the expected values of some unknown quantities, given the 427 values of some correlated, known quantities. EM assumes that 428 the quantities are represented as values in some parameterized 429 probability distribution such as the MVN distribution. The 430 EM algorithm involves two main steps. The two steps are 431 the Expectation Step and the Maximization Step. A general 432 framework for the EM algorithm is given as following. 433

- 1) Initialize the MLEs of distribution parameters ( $\hat{\mu}$ and  $\hat{\Sigma}$ ).
- 2) Repeat until convergence.

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- a) *E-Step:* estimate the Expected value of the missing 437 measurements given the current MLEs ( $\hat{\mu}$  and  $\hat{\Sigma}$ ) 438 of the distribution.
- b) *M-Step*: given the expected estimates of the missing measurements, re-estimate the distribution parameters ( $\hat{\mu}$  and  $\hat{\Sigma}$ ) to Maximize the likelihood of 442 the data.

Because the results of the E-Step and M-Step depend 444 on each other, the EM algorithm is iterated a number of 445 times until the convergence of  $\hat{\mu}$  and  $\hat{\Sigma}$ . The key to the 446 success of the EM algorithm lies in the operation of the 447 E-Step. To understand how the estimation is carried out in 448 the E-Step, it is necessary to introduce some notation. For 449 an observation vector  $\mathbf{w}_i$  with some missing values, let  $\mathbf{w}_i^u$ 450 denote the *unknown* or *missing* measurements, and let  $\mathbf{w}_{i}^{k}$ 451 denote the *known* measurements. Thus,  $w_i$  can be partitioned 452 as  $\mathbf{w_j} = \begin{bmatrix} \mathbf{w_j^u} \\ \mathbf{w_j^k} \end{bmatrix}$ , and accordingly  $\hat{\mu}$  and  $\hat{\Sigma}$  can be partitioned as  $\hat{\mu} = \begin{bmatrix} \hat{\mu}^u \\ \hat{\mu}^k \end{bmatrix}$  and  $\hat{\Sigma} = \begin{bmatrix} \hat{\Sigma}_{u,u} & \hat{\Sigma}_{u,k} \\ \hat{\Sigma}_{k,u} & \hat{\Sigma}_{k,k} \end{bmatrix}$ . Then, the conditional probability of  $\mathbf{w_j^u}$  given  $\mathbf{w_j^k}$  is normal, where the 453 454 455

nean of 
$$\mathbf{w}_{\mathbf{j}}^{\mathbf{u}} = \hat{\mu}^{\mathbf{u}} + \hat{\Sigma}_{u,k} \hat{\Sigma}_{k,k}^{-1} (\mathbf{w}_{\mathbf{j}}^{\mathbf{k}} - \hat{\mu}^{\mathbf{k}}).$$
 (16)

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Fig. 8. Estimating skipped PSRO measurements.

Equation (16) is the key method used for the E-Step, and 456 it can be intuitively explained as follows (formal derivations 457 can be found in [10] and [16]). The equation basically says 458 that the expected values for the missing measurements of 459 wafer j are equal to the estimated means at their locations  $\hat{\mu}^{\mathbf{u}}$ 460 plus some term that estimates the deviations of the unknown 461 measurements of wafer *i* from their mean  $\hat{\mu}^{\mathbf{u}}$ . This term is 462 equal to the deviations of the known measurements of wafer *j* 463 from their mean  $(\mathbf{w}_{\mathbf{i}}^{\mathbf{k}} - \hat{\mu}^{\mathbf{k}})$  multiplied by some weight. This 464 weight is the product of: 465

<sup>466</sup> 1) the covariance,  $\hat{\Sigma}_{u,k}$ , between the unknown and the <sup>467</sup> known measurements, which reflects the dependencies <sup>468</sup> between the known measurements and the unknown <sup>469</sup> measurements;

2) the inverse of the variance of the known measurements  $\hat{\Sigma}_{k,k}^{-1}$ , which reduces the contribution of the known measurements toward the estimation if they have large variances, and consequently they should not be quite "trusted."

For example, Fig. 8(a) shows a visual imagery of the 475 PSRO variability measurements if all the sites of a wafer 476 are measured. Fig. 8(b) shows the measured variability after 477 skipping the measurements of 68 die leading to a reduction 478 factor of  $2.53 \times$  in the number of required measurements. 479 Fig. 8(d) shows the estimations of the skipped measurements 480 with an average estimation error of 0.69%. Fig. 8(c) shows the 481 test results after skipping the measurements of 93 die. Fig. 8(e) 482 gives the expected estimations of the skipped measurements 483 with an average estimation error of 1.17%. 484

Another interesting aspect of the EM algorithm is that it also computes the covariance of the estimated measurements as follows:

covariance of 
$$\mathbf{w}_{\mathbf{i}}^{\mathbf{u}} = \hat{\Sigma}_{u,u} - \hat{\Sigma}_{u,k} \hat{\Sigma}_{k,k}^{-1} \hat{\Sigma}_{k,u}.$$
 (17)

The diagonal elements of the covariance matrix of (17) give 488 the variances in the estimations of the missing measurements. 489 The ability of the EM algorithm to calculate the variances in 490 its estimations is useful as it gives the confidence intervals of 491 the EM algorithm in its estimation of a skipped measurement. 492 Small variances indicate that the EM algorithm is confident 493 in its estimation of the missing measurements, and large 494 variances indicate that the estimated values for the missing 495 measurements might significantly deviate from their true val-496 ues. The calculated variance of an estimated measurement 497 provides a *confidence interval* where the true measurement of 498 a skipped site would likely fall. These confidence intervals 499 provide "safety nets" for the estimated measurements. For 500 example, if we denote an estimated measurement of a skipped 501 site by  $m_s$  and the variance in estimation by  $\sigma_{s,s}$  then one can 502 be almost certain that the true value of the measurement at the 503 skipped site will fall between  $m_s - 3\sqrt{\sigma_{s,s}}$  and  $m_s + 3\sqrt{\sigma_{s,s}}$ . 504

## B. Empirical Validation

1) *First Experiment—Random Sampling:* To illustrate the performance of the proposed EM-based spatial estimation algorithm, we conduct a first experiment where we assess the accuracy of the estimations as a function of the percentage of skipped variability measurements. The main steps of our experiment are given as following.

- We process all original wafers and intentionally delete from each wafer a fixed percentage of its measurements. The locations of these deleted measurements are random both within each wafer and across all wafers.
- We run the EM algorithm to estimate the expected values of all deleted measurements. The EM algorithm only uses the modified wafers; i.e., it has no information about the original set of measurements.
- 3) We compare the EM estimations against the measurements from the original wafers.

We report in Table I the main comparison results of the EM 522 algorithm as a function of the percentage of the measurements 523 deleted. We report these comparison results for both the PSRO 524 and leakage measurements. These results include: 1) the nor-525 malized average estimation error where the estimation error 526 is defined as the absolute difference between an estimation 527 and its true value normalized with respect to the true value; 2) 528 the normalized standard error which is the standard deviation 529 of the estimation errors normalized to the standard deviation 530 of the measurements; 3) the estimation error threshold where 531 50% of the all estimations lie below it (i.e., median error); and 532 4) the estimation error where 95% of the all estimations lie 533 below it. For clarification, the first line in the table indicates 534 that the average estimation error is 0.59% when 10% of the 535 measurement are deleted, and that 50% of the estimation errors 536 are below 0.36%, and that 95% of the estimation errors are 537 below 1.85%. For leakage power measurements, we apply the 538 appropriate Box-Cox transformations before the execution of 539 the EM algorithm, and then apply the inverse transformations 540 after the EM algorithm computes the estimated measurements. 541

The results in Table I lead to a number of insightful 542 conclusions on the performance of the proposed EM algorithm 543

505

520

TABLE I Statistics of Estimation Errors for PSRO and Leakage Skipped Measurements Using Random Sampling Within and Across All Wafers

Skipped	PSRO				Leakage			
	Normalized	Normalized	Error Threshold		Normalized	Normalized	Error Thr	eshold
	Avg. Error	Std. Dev.	50%	95%	Avg. Error	Std. Dev.	50%	95%
10%	0.59%	1.65%	0.36%	1.84%	3.91%	0.17%	2.59%	11.69%
20%	0.69%	1.73%	0.43%	2.15%	4.91%	0.97%	3.02%	13.37%
30%	0.71%	1.79%	0.44%	2.17%	4.88%	0.21%	3.15%	14.93%
40%	0.73%	1.85%	0.46%	2.24%	5.24%	0.24%	3.36%	16.10%
50%	0.75%	1.92%	0.49%	2.21%	5.53%	0.26%	3.54%	17.12%
60%	0.77%	1.95%	0.51%	2.27%	6.04%	0.31%	3.85%	18.87%
70%	0.86%	2.00%	0.59%	2.45%	7.04%	0.89%	4.29%	20.70%
80%	1.06%	2.17%	0.78%	2.86%	9.56%	2.11%	5.75%	24.97%
90%	1.98%	3.30%	1.50%	5.45%	16.44%	1.75%	11.22%	44.20%

We report the following metrics: normalized average estimation error (normalized average error), normalized standard error (normalized standard deviation), and the estimation error threshold (error threshold).



Fig. 9. Sampling plan across all wafers with a pitch of 3. Red locations are sampled. Green locations are skipped.

544 as a spatial estimator.

- The tight values in estimation errors demonstrate that
  the proposed EM algorithm provides very accurate estimation results even when large numbers of the measurements are skipped. Thus, it is possible to used the
  proposed method to drastically cut down the required
  parametric measurements.
- 2) The true values of the vast majority (95%) of the skipped
   PSRO measurements lie within a tight range of the
   estimations provided by the EM algorithm which further
   confirmins the accuracy of the proposed method.
- 3) While the leakage estimations appear to be less accu-555 rate than the estimations of the PSRO measurements 556 in absolute values, the errors in leakage estimation 557 measurements are relatively better as measured by the 558 normalized standard deviation. That is, the spread in 559 leakage estimation errors normalized by the overall 560 spread in leakage measurements is much smaller than 561 the spread of PSRO estimation errors normalized by the 562 overall spread in PSRO measurements. In general, it is 563 necessary to be careful when power data transformations 564 are applied because small errors in estimations might be 565 magnified after the appropriate inverse data transforma-566 tions are applied. 567

<sup>568</sup> 2) *Second Experiment—Identical Sampling:* In our first <sup>569</sup> experiment the skipped or missing measurements were



Fig. 10. Error in estimation as function of the available number of wafers.

random across all wafers, and thus, accurate estimation is 570 possible as the locations of the missing measurements of 571 one wafer are likely to have measurements in some other 572 wafers. In some cases (e.g., in process control monitoring), 573 the locations of the sampled measurements and the skipped 574 locations are the same across all wafers. For example, Fig. 575 9 shows a sampling plan with a *pitch* of 3. In this case, 576 to estimate the electrical characteristics at wafer locations 577 that are not sampled, it is necessary to know the mean  $(\mu)$ 578 and covariance structure  $(\Sigma)$  in order to utilize (16). The 579 mean and covariance structures could be obtained from either 580 previously fully characterized wafers or using empirical 581 models (e.g., variograms) based on historical fabrication data. 582 In this second experiment, we repeat experiment 1 but using 583 identical wafer sampling plans with different pitches. We 584 report the results in Table II. The results show competitive 585 estimation results with the random sampling results (Table I) 586 of the first experiment. In this experiment, we used the 587 known mean and covariance structures before sampling the 588 measurements. 589

3) *Third Experiment—Impact of Data Set Size:* As a third experiment, we assess the accuracy of EM-based estimation as a function of the available number of wafers. It is expected that the larger the number of available wafer, the more accurate the estimation will be. This experiment attempts to quantify this intuitive reasoning. For this experiment, we delete 20% of the measurements as outlined in the first

Kriging

EM

SAMPLING ACROSS ALL WAFERS									
Pitch	Skipped	PSRO			Leakage				
		Normalized	Normalized	Error Threshold		Normalized	Normalized	Error Threshold	
		Avg. Error	Std. Dev.	50%	95%	Avg. Error	Std. Dev.	50%	95%
2	76.58%	0.56%	1.30%	0.38%	1.55%	4.23%	0.18%	2.63%	13.71%
3	89.19%	0.94%	2.09%	0.59%	3.12%	7.65%	0.39%	4.15%	27.83%
4	94.59%	1.12%	2.07%	0.82%	3.02%	7.91%	0.29%	5.48%	23.32%
5	96.40%	1.38%	2.46%	1.05%	3.68%	9.86%	0.34%	7.06%	28.52%

STATISTICS OF ESTIMATION ERRORS FOR PSRO AND LEAKAGE SKIPPED MEASUREMENTS USING IDENTICAL

We report the following metrics: normalized average estimation error (normalized average error), normalized standard error (normalized standard deviation), and the estimation error threshold (error threshold).

Cubic b-spline interpolation



Fig. 11. Performance of EM algorithm vs. Kriging estimation and cubic b-spline interpolation in estimated skipped PSRO parametric test measurements.



Fig. 12. Performance of EM algorithm vs. Kriging estimation and cubic b-spline interpolation in estimating skipped leakage parametric test measurements.

experiment, and then we execute the EM algorithm on: 598 1) the entire 244 wafers; and 2) two independent subsets of 599 122 wafers each. We report the absolute average estimation 600 error as a function of the number of wafers in Fig. 10. The 601 results show that using all wafers toward the estimations 602 reduces the error from an average of 0.92% to 0.69% for 603 the PSRO measurements and from 6.82% to 4.91% for the 604 leakage measurements. 605

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4) Fourth Experiment-Comparisons: It is informative 607 to compare the estimation accuracy of the proposed EM-608 based algorithm against existing variability spatial estimation 609 techniques. For this purpose, we implement both the Kriging 610 algorithm as summarized in Section II-A, and classical cubic 611 b-spline interpolation. Cubic b-spline interpolation estimates 612 the skipped measurements by convolving a 2-D cubic b-spline 613 function with the field measurements. In contrast to the pro-614 posed EM method, these methods use only the measurements 615 of a given wafer to interpolate the missing measurements 616 in the same wafer. Such approaches have an advantage of 617 computational runtime but they suffer from low accuracy, 618 especially when the number of missing measurements in a 619

wafer increases. We report in Figs. 11 and 12 the average 620 absolute error of the EM algorithm PSRO and leakage esti-621 mations, respectively, vs. the estimations of both the Kriging 622 estimation and cubic b-spline interpolations. The results show 623 a stable, superior performance of the EM algorithm in com-624 parison to existing methods. Kriging estimation and cubic b-625 spline interpolations deteriorate drastically when only a few 626 of measurements are available. 627

Our method is more powerful than spatial-based Kriging 628 estimators [18] because it makes use of the entire data set, 629 i.e., measurements from all wafers, to estimate the missing 630 measurements for each wafer. Note that our method does not 631 make any explicit use of the spatial locations on the wafers, so 632 even if the measurements are permuted across all wafers in the 633 same manner (e.g., measurements of two locations across all 634 wafers are swapped), our method will still correctly estimate 635 the missing values. Kriging estimators are good for geospatial 636 studies where there is typically only one set of measurements 637 on a given spatial field; however, in semiconductor fabrication 638 there are typically hundreds and thousands of wafers that 639 are generated roughly using the same process. Our proposed 640 method exploits the variance-covariance structure between the 641

TABLE II

TABLE III COMPARISON BETWEEN DIFFERENT ESTIMATION TECHNIQUES

	Expectation-Maximization	Kriging	Spatial Interpolation		
Advantages	<ul> <li>extremely accurate estimation</li> <li>tolerates large volume of missing data</li> <li>provides uncertainty estimates</li> <li>works even with lack of spatial structure</li> </ul>	<ul> <li>accurate only for few missing measurements</li> <li>computational runtime</li> <li>provides uncertainty estimates</li> </ul>	<ul> <li>accurate only when few missing measurements are present</li> <li>computational runtime</li> </ul>		
Disadvantages	<ul> <li>computational runtime</li> <li>needs sizable number of wafers to compute accurate results</li> </ul>	<ul> <li>accuracy deteriorates with larger volume of missing data</li> <li>fails when no spatial structure exists</li> </ul>	<ul> <li>accuracy deteriorates with larger volume of missing data</li> <li>fails when no spatial structure exists</li> <li>no guarantee on estimation certainty</li> <li>worst estimation accuracy</li> </ul>		



Fig. 13. Decomposition of the process variation into systematic and random residuals.

various measurements on the different wafers to estimate 642 the missing measurements. Table III provides a summary of 643 comparison between the proposed EM-based spatial estimation 644 technique and previous approaches. Furthermore, our model is 645 more accurate and versatile than deterministic approaches [25] 646 that attempt to fit a deterministic mathematical model that is 647 a function of spatial location onto a given data set. In contrast 648 to deterministic approaches, our approach does not require 649 calculating fitting constants that generally change depending 650 on the wafer, and besides estimating the measurements, it 651 provides confidence intervals for the estimations. 652

## 653 V. PROPOSED VARIABILITY DECOMPOSITION TECHNIQUE

Given the completed data set and the MLEs ( $\hat{\mu}$  and  $\hat{\Sigma}$ ) of 654 the underlying MVN distribution, the objective of this section 655 is: 1) to carry out further analysis on the spatial structure 656 of both  $\hat{\mu}$  and  $\hat{\Sigma}$ ; and 2) to provide hierarchical variability 657 decomposition techniques. The vector  $\hat{\mu} = (\hat{\mu}_1, \dots, \hat{\mu}_p)$ 658 gives the mean value of the parameter under test for 659 every location on the wafers; thus,  $\hat{\mu}$  can be regarded as 660 the result of the systematic manufacturing sources, and it 661 represents the expected wafer measurements across the entire 662 process. On the other hand, the residual of each wafer, 663  $\mathbf{r_j} = \mathbf{w_j} - \hat{\mu} = (x_{j1} - \hat{\mu}_1, x_{j2} - \hat{\mu}_2, \dots, x_{jp} - \hat{\mu}_p)$ , can be 664 considered as the result of the process variations that impact 665 each wafer differently. These residuals form a spatial field 666

with a correlation structure between its variables at the 667 different p locations. This correlation structure is captured 668 by the covariance matrix  $\hat{\Sigma}$  of the MVN distribution. Fig. 13 669 shows the measurements of two wafers broken into two 670 parts: 1) the  $\hat{\mu}$  part which is shared between the two wafers 671 (and any other wafer as well); and 2) the random residual 672 part which is unique for every wafer. Thus, a measurement 673  $x_{ii} = \mu_i + r_{ij}$  on wafer j and location (or die) i is the sum 674 of two numbers that are contributed from the systematic and 675 variability sources. 676

## A. Variability Decomposition Using Nested ANOVA

To decompose the variability sources into a hierarchy of lotto-lot, wafer-to-wafer, die-to-die and within-die variations, we consider a four-factor nested ANOVA model. If  $r_{lkji}$  denotes the random residual at site  $i \in \{1, \dots, s\}$  of die  $j \in \{1, \dots, p\}$ located on wafer  $k \in \{1, \dots, w\}$  of lot  $l \in \{1, \dots, L\}$  then the nested ANOVA model decomposes variability as

$$r_{lkji} = \tau_l + \beta_{k(l)} + \gamma_{j(kl)} + \epsilon_{i(jkl)}$$
(18)

677

678

where  $\tau_l$  is the lot effect, and  $\beta_{k(l)}$  is the wafer effect nested under the *l*th lot effect,  $\gamma_{j(kl)}$  are the die *j* effect nested under the *k*th die and *l*th wafer effects, and finally  $\epsilon_{i(jkl)}$  is the site *i*th effect nested under the *j*th die, the *k*th wafer, and the *l*th lot effects. The sum of squares due to lot-lot variability is given by



Fig. 14. Proposed flow for variability decomposition.

$$SS_{lot-to-lot} = w \times p \times s \sum_{l} (\bar{r}_{l...} - \bar{r}_{...})^2$$
(19)

$$= w \times s \times p \sum_{l} (\bar{r}_{l...})^2$$
(20)

with L-1 degrees of freedom and where  $r_{l...} = \sum_{k} \sum_{j} \sum_{i} r_{lkji}$ . Note that (19) has been simplified to (20) because we already decomposed the measurement data into systematic and residual variability sources which leads to residuals with a mean of zero. The sum of squares due to wafer-to-wafer variability is given by

$$SS_{wafer-to-wafer} = p \times s \sum_{l} \sum_{k} (\bar{r}_{lk..} - \bar{r}_{l...})^2$$
(21)

with  $L \times (w - 1)$  degrees of freedom and where  $r_{lk..} = \sum_{j} \sum_{i} r_{lkji}$ . The sum of squares of due to die-to-die variability

$$SS_{die-to-die} = s \sum_{l} \sum_{k} \sum_{j} (\bar{r}_{lkj.} - \bar{r}_{lk..})^2$$
 (22)

with  $L \times w \times (p-1)$  degrees of freedom and  $r_{lkj.} = \sum_{i} r_{lkji}$ . Finally within-die variability is given by

$$SS_{within-die} = \sum_{l} \sum_{k} \sum_{j} \sum_{i} (r_{lkji} - \bar{r}_{lkj.})^2$$
(23)

with  $p \times L \times w \times (s-1)$  degrees of freedom. The total vari-700 ability  $SS_T = \sum_l \sum_k \sum_j \sum_i (r_{lkji} - \bar{r}_{...})^2 = \sum_l \sum_k \sum_j \sum_i r_{lkji}^2$ with Lwps - 1 degrees of freedom. Thus, the contribu-701 702 tion of lot-to-lot variability is equal to  $SS_{lot-to-lot}/SS_T$ ; 703 the contribution of wafer-to-wafer variability is equal to 704  $SS_{wafer-to-wafer}/SS_T$ , the contribution of die-to-die variability 705 is equal to  $SS_{die-to-die}/SS_T$ , and finally the within-die con-706 tribution is  $SS_{within-die}/SS_T$ . We compute these contributions 707 for our data set after filling all missing values in accordance 708 with the flow of Fig. 14 and give the results in the pie chart 709 given in Fig. 15. 710

Variability decomposition by source



Fig. 15. Budgeting the contribution of process variations from within-die, die-to-die, wafer-to-wafer, and lot-to-lot sources.

## B. Spatial Analysis of Systematic Sources

The vector  $\hat{\mu}$  gives the mean of the PSRO frequency mea-712 surements at each wafer location as illustrated in Fig. 16(a). 713 The spatial dependency in  $\hat{\mu}$  can be observed by plotting the 714 values of  $\hat{\mu}$  as a function of the radius from the center of 715 the wafer as shown in Fig. 16(b). The plot shows that the 716 average values of the measurements generally decreases as the 717 distance from the center increases. This systematic dependency 718 can be also expressed by a quadratic function where the 719 systematic component  $u_i$  at location (x, y) can be captured 720 as  $\mu_i = ax^2 + by^2 + cxy + dx + ey + f$ , where a, b, c, d, e, and f 721 are constants that can be found using least square fitting [25]. 722

## C. Spatial Analysis of Random Sources

To reveal insights into the wafer-level spatial structure of 724 the residuals, we propose using experimental variograms. Var-725 iograms can reveal any spatial trends in the random variations. 726 Variograms have been previously proposed in the literature to 727 analyze the spatial trends of within-die process variations [13], 728 [18], [28]. In experimental variograms plots, the variance in 729 measurements is plotted as function of the distance, or lag h, 730 between them. Since, we have subtracted the mean  $\hat{\mu}$  from the 731 measurements, we can directly express the variogram function 732 of the residuals of some wafer *j* as follows: 733

$$\gamma_j(h) = \frac{1}{2N_j(h)} \sum_{N_j(h)} (r_j(l) - r_j(l+h))^2$$
(24)

where  $r_i(l)$  is the residual of wafer *j* at location *l*,  $r_i(l+h)$ 734 is the residual of wafer j at location l + h, and  $N_i(h)$  gives 735 the number of measurements that are at distance h from 736 each other on wafer j. If there is a spatial structure in the 737 data, then we would expect nearby measurements to have 738 similar values, and thus,  $\gamma_i(h)$  would be close to zero in this 739 case. As the distance between the measurements increases, 740 the measurements would be more independent, and thus, 741  $\gamma_i(h)$  would increase (potentially leveling up at a particular 742 value). Fig. 17 gives the variograms of wafers from three 743 different lots. The variograms reveal a spatial correlation 744 structure in the random residuals, where the independency in 745 the measurements increases exponentially (or equivalently the 746 dependency between the measurements decays exponentially) 747 as the distance between the measurements increases. The 748 computed experimental variograms show the same trends as 749 the synthetic theoretical variograms plotted earlier in Fig. 1. 750 More interestingly, Fig. 17 shows that variograms of wafers 751

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Fig. 16. (a) Mean of the parametric measurements ( $\hat{\mu}$ ) as a function of its location on the wafer. (b) Average value (speed) of the parameter under test as a function of the distance (radius) from the center of the wafer.



Fig. 17. Variograms for the wafers in the first three lots. The first lot variograms are given by the solid blue lines. The second lot variagrams are given by the red dashed lines. The third lot variograms are given by the green short dotted lines.

that come from the same lot share shape similarities more than
 variograms of wafers in different lots.

754

## VI. CONCLUSION AND FUTURE WORK

In this paper, we proposed a novel modeling technique 755 to characterize process variability parametric measurements 756 based on statistical multivariate techniques. We proposed 757 using the expectation-maximization algorithm to estimate the 758 expected values of the missing test measurements, and to 759 accurately estimate the statistical model parameters. Using chi-760 square plots, we proposed techniques to verify the accuracy 761 of our model and to detect any outliers. We also carried out 762 further analysis to reveal spatial trends in the systematic and 763 random sources of the variations. We elucidated the trade-764 off between direct measurements and estimation as a function 765 of the number of available measurements, and contrasted the 766 performance of our proposed method against other spatial 767 estimation techniques such as geospatial Kriging estimation 768 and traditional cubic b-spline interpolation. Finally, we pro-769 posed a variability decomposition flow that uses our spatial 770

estimation techniques together with nested ANOVA method, to split the variability into lot-to-lot, wafer-to-wafer, die-todie, and within-die components. We quantified each of these components and provided analysis techniques to uncover their spatial structure.

Future work includes researching further applications for 776 our techniques. For example, one possible application is part-777 average testing (PAT) which is used to remove electronic 778 parts with abnormal characteristics from the semiconductors 779 supplied to the automative electronic industry. Most PAT ap-780 plications assume Gaussian distribution for the electrical mea-781 surements. In case the electrical measurements (e.g., leakage) 782 are not Gaussian, one possible future work is to incorporate 783 the Box-Cox transformation into PAT procedures. 784

## ACKNOWLEDGMENT

We thank the anonymous reviewers for their comments 786 which improved many sections of this paper. 787

## REFERENCES

- M. Bhushan, A. Gattiker, M. Ketchen, and K. K. Das, "Ring oscillators for CMOS process tuning and variability control," *IEEE Trans. Semicond. Manuf.*, vol. 19, no. 1, pp. 10–18, Feb. 2006.
- [2] D. Boning and S. Nassif, "Models of process variations in device and interconnect," in *Design of High-Performance Microporcessor Circuits*, A. Chandrakasan, W. J. Bowhill, and F. Cox, Eds., 1st ed. Piscataway, NJ: IEEE Press, 2001, pp. 98–115.
- [3] S. Borkar, "Designing reliable systems from unreliable components: The challenges of transistor variability and degradation," *IEEE Micro*, vol. 25, no. 6, pp. 10–16, Nov.–Dec. 2005.
- [4] S. Borkar, T. Karnik, S. Narendra, J. Tschanz, A. Keshavarzi, and V. De, "Parameter variations and impact on circuits and microarchitecture," in *Proc. Design Autom. Conf.*, 2003, pp. 338–342.
- [5] K. Bowman, S. Duvall, and J. Meindl, "Impact of die-to-die and withindie parameter fluctations on the maximum clock frequency distribution for gigascale integration," *IEEE J. Solid State Electron.*, vol. 37, no. 2, pp. 183–190, Feb. 2002.
- [6] G. Box and D. Cox, "An analaysis of transformations," J. Royal Statistical Soc., Ser. B, vol. 26, no. 2, pp. 211–252, 1964.

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- [7] J. Cain and C. Spanos, "Electrical linewidth metrology for systematic
  CD variation characterization and causal analysis," in *Proc. SPIE*, vol.
  5038. 2003, pp. 350–361.
- [8] Y. Cao, P. Gupta, A. B. Kahng, D. Sylvester, and J. Yang, "Design sensitivities to variability: Extrapolation and assessments in nanometer VLSI," in *Proc. IEEE ASIC/SoC Conf.*, 2002, pp. 411–415.
- [9] C. Cho, D. Kim, J. Kim, J. Plouchart, D. Lim, S. Cho, and R. Trzcinski,
  "Decomposition and analysis of process variability using constrained principal component analysis," *IEEE Trans. Semicond. Manuf.*, vol. 21,
  no. 1, pp. 55–62, Feb. 2008.
- 818 [10] A. Dempster, N. Laird, and D. Rubin, "Maximum likelihood from incomplete data via the EM algorithm," *J. Royal Statistical Soc., Ser. B*, vol. 39, no. 1, pp. 1–38, 1977.
- [11] P. Friedberg, Y. Cao, J. Cain, R. Wang, J. Rabaey, and C. Spanos,
  "Modeling within-die spatial correlation effects for process-design cooptimization," in *Proc. Int. Symp. Quality Electron. Design Autom.*,
  2005, pp. 516–521.
- [12] A. Gattiker, "Unraveling variability for process/product improvement,"
   in *Proc. Int. Test Conf.*, 2008, pp. 1–9.
- B. Hargreaves, H. Hult, and S. Reda, "Within-die process variations: How accurately can they be statistically modeled?" in *Proc. Asia South Pacific Design Autom. Conf.*, 2008, pp. 524–530.
- [14] International Technology Roadmap for Semiconductors. (2007) [Online].
   Available: http://public.itrs.net
- [15] J. Jin and H. Guo, "ANOVA method for variance component decomposition and diagnosis in batch manufacturing processes," *Int. J. Flexible Manuf. Syst.*, vol. 15, no. 2, pp. 167–186, 2003.
- [16] R. A. Johnson and D. W. Wichern, *Applied Multivariate Statistical Analysis*, 6th ed. Englewood Cliffs, NJ: Prentice Hall, 2007.
- [17] H. R. Lindman, Analysis of Variance in Experimental Design. Berlin,
   Germany: Springer-Verlag, 1992.
- [18] F. Liu, "A general framework for spatial correlation modeling in VLSI design," in *Proc. Design Autom. Conf.*, 2007, pp. 817–822.
- [19] J. A. Nelder and R. Mead, "A simplex method for function minimization," *Comput. J.*, vol. 7, no. 4, pp. 308–313, 1965.
- [20] A. Nieuwoudt and Y. Massoud, "Assessing the implications of process
   variations on future carbon nanotube bundle interconnect solutions," in
   *Proc. Int. Symp. Quality Electron. Design*, 2007, pp. 119–126.
- 846 [21] M. Orshansky, L. Milor, and C. Hu, "Characterization of spatial intrafield gate CD variability, its impact on circuit performance, and spatial mask-level correction," *IEEE Trans. Semicond. Manuf.*, vol. 17, no. 1, pp. 2–11, Feb. 2004.
- [22] M. Orshansky and S. Nassif, *Design or Manufacturability and Statistical Design: A Constructive Approach*. Berlin, Germany: Springer, 2007.
- [23] S. Reda and S. Nassif, "Analyzing the impact of process variations on parametric measurements: Novel models and applications," in *Proc. Design, Autom. Test Eur. Autom.*, 2009, pp. 375–380.
- [24] O. Schabenberger and C. A. Gotway, *Statistical Methods for Spatial Data Analysis*, 1st ed. London, U.K.: Chapman and Hall, 2004.
- B. E. Stine, E. Chang, D. S. Boning, and J. E. Chung, "Analysis and decomposition of spatial variation in integrated circuit processes and devices," *IEEE Trans. Semicond. Manuf.*, vol. 10, no. 1, pp. 24–41, Feb. 1997.

- [26] D. Sylvester, K. Agarwal, and S. Shah, "Variability in nanometer CMOS: Impact, analysis, and minimization," *VLSI J. Integr.*, vol. 41, no. 3, pp. 319–339, 2008.
- [27] B. Wong, A. Mittal, Y. Cao, and G. W. Starr, *Nano-CMOS Circuit and Physical Design*. New York: Wiley-Interscience, 2004.
- [28] J. Xiong, V. Zolotov, and L. He, "Robust extraction of spatial correlation," in *Proc. Int. Symp. Physical Design*, 2006, pp. 2–9.



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